HITACHI

New Basic

RSO Real-time & Storage Oscilloscope

Power of Both Analog & Digital

VC-6745A
40MS/s(1CH), 20MS/s(2CH simultaneously),
100MHz, 4kw(1CH), 2kw/CH

VC-6725A
20MS/s (2CH simultaneously),
50MHz, 2kw/CH

VC-6724A
20MS/s, 50MHz, 2kw/CH

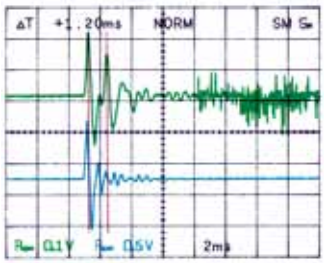
VC-6723A
20MS/s, 20MHz, 2kw/CH

Easy to Use Compact Analog Real-t

Single Shot Capture

Elusive single-shot and intermittent phenomena are simple to capture using the digital storage function.

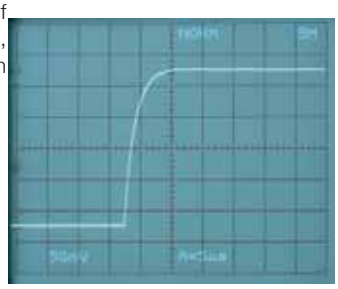
	Max. sampling rate
VC-6745A	40MS/s(1CH), 20MS/s(2CH simultaneously)
VC-6725A	20MS/s(2CH simultaneously)
VC-6724A	20MS/s
VC-6723A	20MS/s



Storage of Repeating Waveform

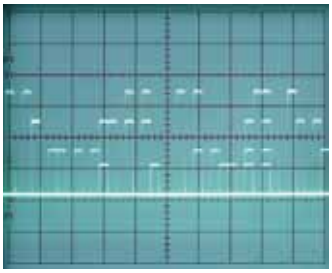
Storage of repetitive waveforms of same bandwidth as analog mode, enabling hardcopy and transfer of high speed repetitive waveform.

	Bandwidth
VC-6745A	100MHz
VC-6725A	50MHz
VC-6724A	50MHz
VC-6723A	20MHz

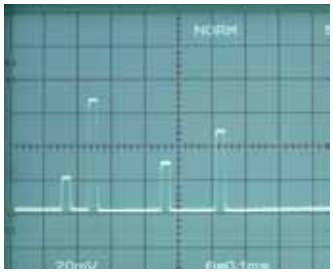


Static Observation of Non-repeating Phenomena

Even non-repeating events which cause a jumbled overlaid display using a conventional oscilloscope can be observed as clean a waveform.



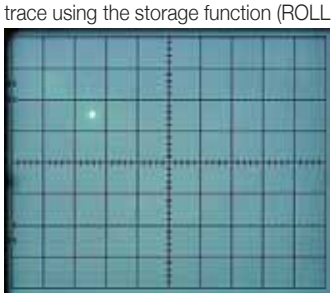
Analog real-time



digital Storage

Observation of Low-speed phenomena

Low speed phenomena which appear as a moving dot can be observed as a trace using the storage function (ROLL MODE).



Analog real-time

digital Storage

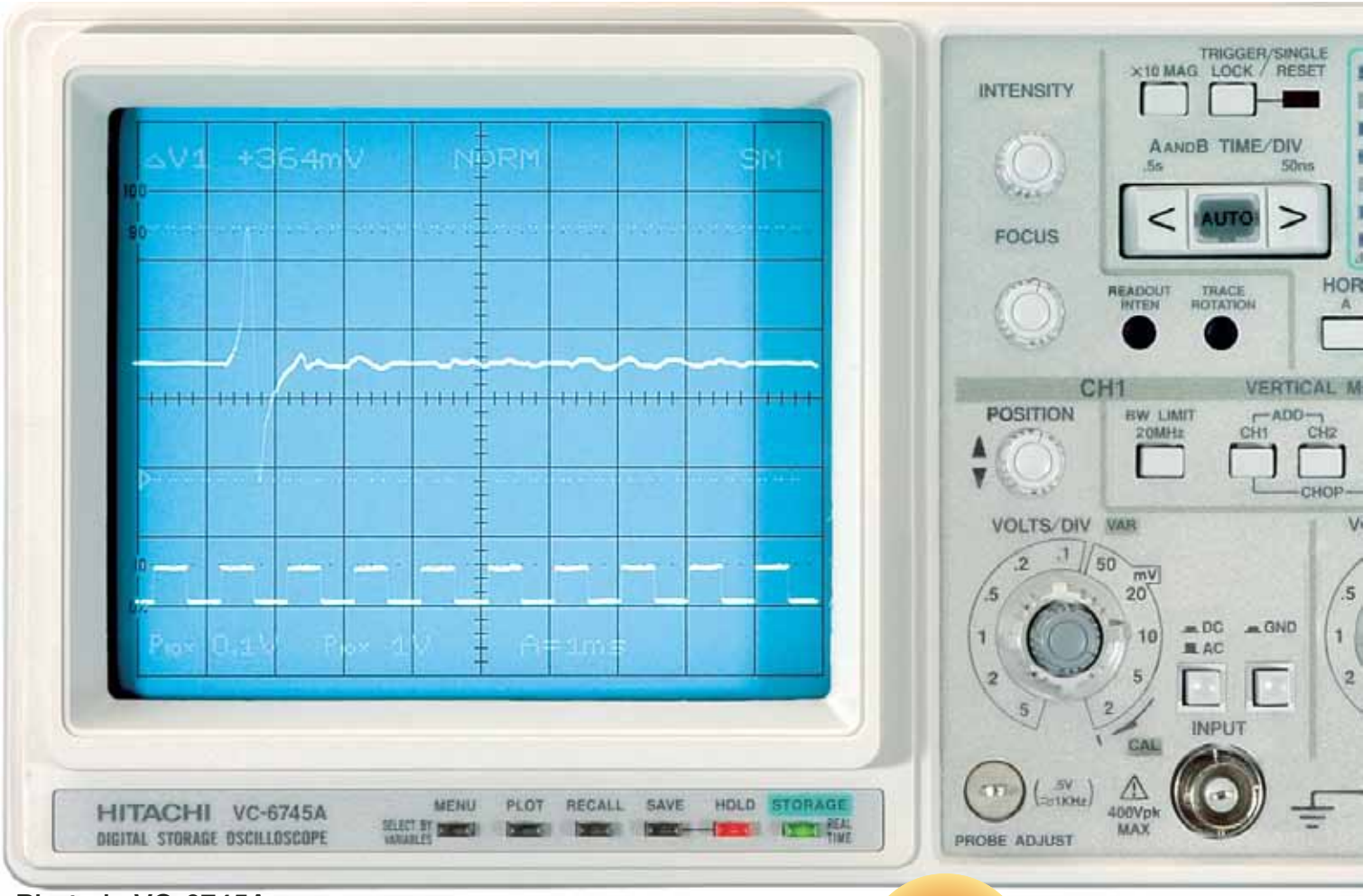


Photo in VC-6745A

DSO Operation is Straightforward by Using Six Push Buttons Located Under the Bezel

Easy Operation

Digital Oscilloscope

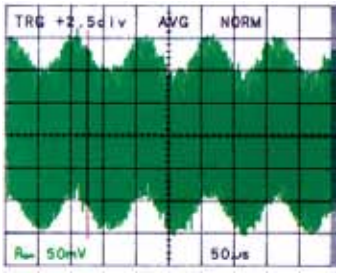
ime & Digital Storage Oscilloscopes

Transfer of Waveform data

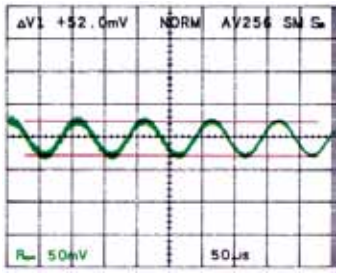
The USB interface can be used to transfer stored waveform data to an external computer or other device for secondary storage and analysis.

Averaging to Reduce Noise

Averaging can be used to reduce noise, thereby creating a display of just the signal of interest.



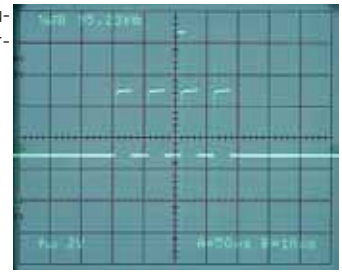
Before averaging



After averaging

Readout/Cursor Measurement

The readout function provides CRT display of measured values. In addition, two cursors can be used to make accurate measurements of voltage difference, time difference, and frequency.



Actual Size

Save Memory

Captured waveform can be stored and recalled for comparison.

Expanded Display

After a waveform is stored, it can be displayed in expanded form. In this display format, data between sampled points can be interpolated (either linear or sinusoidal interpolation can be selected).

Sweep Time Autoranging (VC-6745A/6725A)

The sweep time rate setting is automatically optimized in accordance with the input signal frequency.

Automatic Trigger Level (VC-6745A/6725A)

The trigger level variable range is automatically optimized in accordance with the input signal amplitude.

Trigger Lock (VC-6745A/6725A)

This unique feature is extremely effective in observation of complex pulse trains.

Frequency Counter (VC-6745A/6725A)

The frequency counter of the channel selected as the trigger signal is automatically counted.

Delayed Sweep (VC-6745A/6725A)

Delayed sweep function can be used to expand display in analog real-time mode and to post-trigger in digital storage mode.

DC Offset (VC-6724A)

The DC offset function can be used to cancel an portion of DC voltage at vertical expanded display. In addition, it can be used to cancel an portion of DC voltage at ultra-low speed signal even under AC coupling lower cut-off frequency.

e That Feels Like an Analog Scope

Selection Table

	Max. Sampling Rate	No. of CH	Bandwidth	Memory Capacity	Delayed Sweep	Cursors
VC-6745A	40MS/s (1CH simultaneously) 20MS/s (2CH simultaneously)	2CH	DC to 100MHz	4kw(1CH), 2kw/CH	YES	YES
VC-6725A	20MS/s (2CH simultaneously)	2CH	DC to 50MHz	2kw/CH	YES	YES
VC-6724A	20MS/s	2CH	DC to 50MHz	2kw/CH	—	YES
VC-6723A	20MS/s	2CH	DC to 20MHz	2kw/CH	—	YES

	Frequency counter	Sweep time autoranging	Auto trigger level	Trigger lock	DC offset	Alternate Magnifier	Dimensions (W×H×D mm/ ins.)	Weight (kg/lbs)
VC-6745A	YES	YES	YES	YES	—	—	275 x 130 x 360 / 10.8 x 5.1 x 14.1	6.5 / 14.3
VC-6725A	YES	YES	YES	YES	—	—	275 x 130 x 360 / 10.8 x 5.1 x 14.1	6.5 / 14.3
VC-6724A	—	—	—	—	YES	YES	310 x 130 x 370 / 12.2 x 5.1 x 14.5	8 / 17.6
VC-6723A	—	—	—	—	—	—	310 x 130 x 370 / 12.2 x 5.1 x 14.5	8 / 17.6

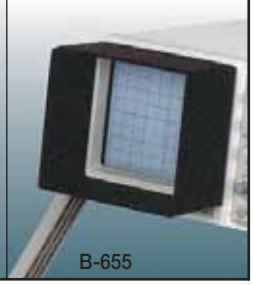
VC-6745A 40MS/s, 100MHz, 2CH

VC-6725A 20MS/s, 50MHz, 2CH

VC-6724A 20MS/s, 50MHz, 2CH

VC-6723A 20MS/s, 20MHz, 2CH

●Optional Accessories

Front Cover	Accessory Pouch	Dust Cover	Viewing Hood
			
No.6806: VC-6724A/6723A No.6809: VC-6745A/6725A	No.6710: VC-6724A/6723A No.6708: VC-6745A/6725A	No. 6519: VC-6745A/6725A No. 6512: VC-6724A/6723A	B-655

VC-6745A/6725A/6724A/6723A Specifications

CRT

Type	6-inch, rectangular
Accelerating potential	VC:6745A: Approx. 17kV VC-6725A/6724A: Approx. 12kV VC-6723A: Approx. 2kV
Z-axis input	DC coupling, positive-going input decreases Bandwidth; VC-6745A/6725A: DC to 5MHz, VC-6724A/6723A: DC to 2MHz Input withstand voltage: 30V (DC+ACpeak) or 30Vp-pAC at 1kHz

VERTICAL SYSTEM

Inputs	CH1, CH2
Sensitivity and accuracy	VC-6745A/6725A: 2mV/div to 5V/div ±3% VC-6724A/6723A: 5mV/div to 5V/div ±3% (×5: 1mV/div)
Bandwidth	VC-6745A: DC to 100MHz (2mV/div: DC to 20MHz) VC-6725A: DC to 50MHz (2mV/div: DC to 10MHz) VC-6724A: DC to 50MHz (×5: DC to 7MHz) VC-6723A: DC to 20MHz (×5: DC to 7MHz)
Rise time	VC-6745A: Approx. 3.5ns (2mV/div: Approx. 17.5ns) VC-6725A: Approx. 7ns (2mV/div: Approx. 35ns) VC-6724A: Approx. 7ns (×5: Approx. 50ns) VC-6723A: Approx. 17.5ns (×5: Approx. 50ns)
Input withstand voltage	VC-6745A/6725A: 400V (DC+ACpeak at 1kHz) VC-6724A/6723A: 300V (DC+ACpeak at 1kHz)
Input coupling	AC, GND, DC
Input impedance	VC-6745A/6725A: 1MΩ±1.5%, approx. 23pF VC-6724A/6723A: Approx. 1MΩ, approx. 25pF
Display mode	VC-6745A/6725A: CH1, CH2, DUAL, CHOP, ADD VC-6724A/6723A: CH1, CH2, ALT, CHOP, ADD
Polarity inversion	CH2 only

X-Y OPERATION

X-axis input	VC-6745A/6725A; X-axis: CH1, CH2, EXT, EXT÷10 (CH1 in storage mode) Y-axis: CH1, CH2, CH1 & CH2 (CH2 in storage mode) VC-6724A/6723A; X--axis: CH1, Y-axis: CH2
X-axis bandwidth	DC to 500kHz
Phase error	Within 3° from DC to 50kHz

HORIZONTAL SYSTEM

Sweep time (non storage mode)	VC-6745A/6725A: A (main) sweep: 50ns/div to 0.5s/div ±3% B (delayed) sweep: 50ns/div to 50ms/div ±3% VC-6724A/6723A: 0.2µs/div to 0.2s/div
Max. sweep rate	VC-6745A/6725A: 5ns/div (×10 mag.) ±4% VC-6724A: 20ns/div (×10 mag.) ±5% VC-6723A: 100ns/div (×10 mag.) ±5% (20ns and 50ns/div are uncalibrated)
Sweep mode	VC-6745A/6725A: A, ALT (non-storage mode only), B VC-6724A/6723A: A only
Delay time	VC-6724A/6725A: 1µs to 5s
Delay jitter	VC-6724A/6725A: 1/20000 or less

TRIGGER SYSTEM

Trigger mode	VC-6745A/6725A: AUTO, NORM, TV-V, TV-H, SINGLE VC-6724A/6723A: AUTO, NORM, TV-V, TV-H
Trigger source	VC-6745A/6725A: CH1, CH2, LINE, EXT (AC, DC, DC÷10) VC-6724A/6723A: INT (CH1, CH2, V-MODE), LINE, EXT
Trigger slope	+, —
Trigger sensitivity	(see tables below)
TV trigger sensitivity	INT: Sync pulse more than 1div EXT: Sync pulse more than 200mVp-p

VC-6745A	DC to 20MHz	20MHz to 100MHz
VC-6725A	DC to 10MHz	10MHz to 50MHz
CH1, CH2	0.35div	1.5div
EXT	50mV	150mV

VC-6724A	20Hz to 5MHz	5MHz to 40MHz	40MHz to 50MHz
VC-6723A	20Hz to 2MHz	2MHz to 20MHz	—
INT(CH1, CH2)	0.5div	1.5div	2.0div
INT(V-MODE)	2.0div	3.0div	3.5div
EXT	200mV	800mV	1V

CRT READOUT FUNCTION

Setting display	CH1/CH2/SAVE sensitivity, Sweep time, Delay time (except VC-6724A/6723A), sampling mode, aliasing condition, trigger point, smoothing, no. of averages, interpolation
Cursor measurements	Voltage difference (ΔV), time difference (ΔT), frequency (1/ΔT)
Frequency counter (except VC-6724A/6723A)	Frequency range; VC-6745A: 20Hz to 100MHz, VC-6725A: 20Hz to 50MHz No. of digits: 4 digits Accuracy: 1 resolution ±100ppm (15 to 35°C)

STORAGE FUNCTION

Max. sampling rate	VC-6745A: 40MS/s (1-CH operation) 20MS/s (2-CH simultaneously) VC-6725A: 20MS/s (2-CH simultaneously) VC-6724A/6723A: 20MS/s
Max. storage bandwidth	VC-6745A: DC to 5MHz (Single shot phenomena) DC to 100MHz (Repetitive phenomena) VC-6725A/6724A: DC to 5MHz (Single shot phenomena) DC to 50MHz (Repetitive phenomena) VC-6723A: DC to 5MHz (Single shot phenomena) DC to 20MHz (Repetitive phenomena)
Memory Capacity	VC-6745A: 4000 word (1CH operation and 2.5µs/div to 50s/div) 2000 word (2CH operation and 2.5µs/div to 50s/div) 1000 word (50ns/div to 2µs/div) VC-6725A: 2000 word (5µs/div to 50s/div) 1000 word (50ns/div to 2µs/div) VC-6724A/6723A: 2000 word (5µs/div to 20s/div 1000 word (0.2µs/div to 2µs/div)
Display memory	1000 word × 4
Save memory	VC-6745A/6725A: 1000 word × 2 (with backed-up) VC-6724A/6723A: 1000 word × 2
Vertical resolution	8 bits
Horizontal display resolution	100 points/div
Storage mode	Normal, Average (4,16, 64, 256 times), Roll, Hold, Single
Sweep time	(see table below)
Smoothing	Selectable On/OFF
Interpolation	Linear or sine (only for magnified display)
Pre-trigger	VC-6745A: Max. 0 to 20div VC-6725A/6724A/6723A: Max. 0 to 10div
Post-trigger	VC-6745A: Max. 0 to 10div
Expanded display	10 times (not possible with respect to saved waveform)
External I/O	USB interface

	Sampling mode	
VC-6745A	Equivalent sampling (A sweep only)	50ns/div to 2µs/div
	A sweep real-time sampling	2.5µs/div to 0.1s/div
	B sweep real-time sampling	2.5µs/div to 50ms/div
	Roll (A sweep only)	0.2s/div to 50s/div
VC-6725A	Equivalent sampling (A sweep only)	50ns/div to 2µs/div
	A sweep real-time sampling	5µs/div to 0.1s/div
	B sweep real-time sampling	5µs/div to 50ms/div
	Roll (A sweep only)	0.2s/div to 50s/div
VC-6724A VC-6723A	Equivalent sampling	0.2µs/div to 2µs/div
	real-time sampling	5µs/div to 0.2s/div
	Roll	0.5s/div to 20s/div

OTHERS

Signal output	Output of the signal selected as the trigger source channel Output voltage: Approx. 25mV/div Frequency response: DC to 10MHz Output impedance: Approx. 50Ω
Power supply	VC-6745A/6725A: 90 to 250V AC, 48 to 440Hz VC-6724A/6723A: 100/120/220/240V AC ±10%, 50/60/400Hz
Ambient temperature	Rated range of use: 10 to 35°C (50 to 95°F) Operating: 0 to 40°C (32 to 104°F) Non-operating: -20 to 70°C (-4 to 158°F)
Ambient humidity	Operation: 45 to 85% Non-operating: 35 to 85% (70 % or less at 50°C (122°F))
EMI protection	Satisfied VDE standard 0871 class B
Power consumption	Approx. 50W
Dimensions	VC-6745A/6725A: Approx. 275(W) ×130(H) × 360(D)mm, 10.8 × 5.1 ×14.2 ins. VC-6724A/6723A: Approx. 310(W) ×130(H) × 370(D)mm, 12.2 × 5.1 ×14.6 ins.
Weight	VC-6745A/6725A: approx. 6.5kg, 14.3 lbs. VC-6724A/6723A; approx. 8kg, 17.6 lbs.

STANDARD ACCESSORIES

Probe (1:1/10:1 switchable) × 2, AC power cord, Software, USB cable, Fuse, Operation manual

Specifications and outer appearance are subject to change without prior notice

Hitachi Denshi Technosystem,Ltd.

Head Office: 393 Megurita-cho, Kodaira city Tokyo 187-0013, Japan
Phone: +81-42-324-8763
Fax: +81-42-323-8536